

FOR IMMEDIATE RELEASE

Contacts: Steve Darrough
Tel: 408-513-1629

ZILOG INTRODUCES ENHANCED DEVELOPER STUDIO II TOOLSET FOR PROGRAMMING EMBEDDED SYSTEM-ON-CHIP SOLUTIONS

Latest Free Toolset Offers Source Code Editing, Debugging and Program Management Capabilities

San Jose, CA, and Biel, Switzerland. June 22, 2010 – Zilog, an IXYS company, (Nasdaq:IXYS) and a trusted supplier of application specific, embedded micro controller (MCU) system-on-chip (SoC) solutions for industrial and consumer markets, today introduced an enhanced Zilog Development Studio II (ZDS II) toolset for programming SoC solutions leveraging Zilog microprocessors.

The ZDS II is a royalty-free, high-end toolset that operates on Windows 98SE/2000/XP/Vista-32 platforms and offers source code editing, debugging and program management all easily accessible from a singly fully-feature integrated development environment. The ZDS II is available now for free download at www.zilog.com.

“Zilog has a rich heritage of providing strong development tools for our customers, and this continues with the Zilog Development Studio II,” said Steve Darrough, Zilog’s Director of Worldwide Marketing. “In particular, the New Project Wizard is designed so that both beginning and advanced users can sit down and quickly develop a new code project faster than ever before.”

ZDS II Technical Details

The ZDS II feature set includes:

- Full ANSI Optimizing C Compiler;
- Macro Assembler;
- Editor;
- Linker and Librarian Tools;
- Project Manager;
- Workspace;
- Source-Level Debugger;
- and Example Code.

Additionally, newly integrated in ZDS II is binary code for Zilog’s RTOS and TCP/IP libraries. Zilog’s RZK RTOS is a preemptive, multitasking, real-time kernel that manages CPU resources to ensure that all time-critical tasks are processed efficiently including:

- Fast context switching;
- Small footprint;

- Modular design;
- Threads with up to 32 priority levels;
- Inter-thread communication options;
- Memory management;
- Ethernet, UART, I2C, SPI, RTC drivers;
- and embedded Flash file system and more.

Finally, the TCP/IP library, ZTP, is a full-featured software suite for the Zilog eZ80 Acclaim!® family of microcontrollers that includes:

- Industry standard RFC compliant protocols;
- Core protocols: IPv4, TCP, UDP, ICMP and ARP;
- And also supports: HTTP/HTTPS, TFTP, SNMP, TELNET, SMTP, DNS, TIMEP, DHCP, FTP and more.

About Zilog, Inc.

Zilog is a trusted supplier of application-specific, embedded system-on-chip (SoC) solutions for the industrial and consumer markets. From its roots as an award-winning architect in the microprocessor and microcontroller industry, Zilog has evolved its expertise beyond core silicon to include SoCs, single board computers, application specific software stacks and development tools that allow embedded designers quick time to market in areas such as energy management, monitoring and metering, motor control and motion detection. Zilog is an IXYS company. For more information, visit our websites at <http://www.ixys.com>, and <http://www.Zilog.com> .

About IXYS Corporation

IXYS Corporation makes and markets technology-driven products to improve power conversion efficiency, generate solar and wind power and provide efficient motor control for industrial applications. IXYS offers a diversified product base that addresses worldwide needs for power control, electrical efficiency, renewable energy, telecommunications, medical devices, electronic displays and RF power.

Safe Harbor Statement

Any statements contained in this press release that are not statements of historical fact, including the performance, features and suitability of products for various applications, may be deemed to be forward-looking statements. There are a number of important factors that could cause the results of IXYS to differ materially from those indicated by these forward-looking statements, including, among others, risks detailed from time to time in the Company's SEC reports, including its Form 10-K for the fiscal year ended March 31, 2010. The Company undertakes no obligation to publicly release the results of any revisions to these forward-looking statements.