

## PRESS RELEASE

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### **IXYS Announces the CPC5002, the Lowest Power Dual High-Speed Optical Logic Isolator with Open-Drain Outputs**

*The CPC5002 is the first device to be offered in the new Digital Optical Isolator family*

Beverly, Massachusetts – June 28, 2012, IXYS Integrated Circuits Division (formerly Clare, Inc.), a wholly owned subsidiary of IXYS Corporation (NASDAQ: IXYS), announced the immediate availability of the first in a new family of high speed communication ICs, the CPC5002 Dual High-Speed, Open Drain Digital Optical Isolator.

The CPC5002 supports 10Mbaud typical data rates for digital communications over an optical barrier while providing 3750Vrms of galvanic isolation. The CPC5002 isolates two logic signals and is available in eight-pin surface mount or DIP packages.

Utilizing a low-power, high-gain CMOS photodetector and requiring only 1.5mA LED drive current, the CPC5002 is the industry's lowest power optical isolator or optocoupler. While most competitive devices work with a minimum supply of 3.0V, the CPC5002 offers a higher degree of design flexibility with guaranteed operation as low as 2.7V.

Unlike magnetically isolated digital isolator ICs, transformers, or capacitive isolators, the CPC5002 optical solution requires no internal clocking (a primary source of EMI/RFI interference in designs) and supports direct coupled logic-level isolation without refresh clocks. In addition, the unique design of the CPC5002 optical isolator offers excellent reliability and long operational life.

"IXYS Integrated Circuits Division, formerly known as Clare, has a long history of providing highly reliable and effective optically isolated solid state relays and optocouplers," said IXYS Integrated Circuits Division President, Mark Heisig. "Our new high speed optical isolators with low power and low generated noise features further complement our overall product offering. With the wide range of IXYS power semiconductors and the Zilog line of high temperature and rugged MCUs, we offer a complete solution for power and energy management products."

The CPC5002 Dual High-Speed Digital Optical Isolator is ideal for clock and data signals in isolated line receiver and bus receiver applications. The device may be used in Power-over-Ethernet (POE) applications, providing buffering and isolation of signals between the host controller and the Power Supply Equipment (PSE) controller. Additional applications include power supply high side interface and isolated signal

monitoring and control. The wide operational power supply range specification of 2.7V to 5.5V enables logic-level translation applications. The CPC5002 is certified to UL 1577 and EN/IEC 60950.

### **About IXYS Integrated Circuits Division and IXYS Corporation**

IXYS Integrated Circuits Division (formerly Clare, Inc.), a leader in the design and manufacture of solid state relays and high voltage integrated circuits, is a wholly owned subsidiary of IXYS Corporation. IXYS Corporation develops and markets primarily high performance power semiconductor devices that are used in controlling and converting electrical power efficiently in power systems for the telecommunication and internet infrastructure, motor drives, medical systems and transportation. IXYS also serves its markets with a combination of digital and analog integrated circuits. Additional information about IXYS Integrated Circuits Division and IXYS may be found at [www.ixysic.com](http://www.ixysic.com) and [www.ixys.com](http://www.ixys.com).

### **Safe Harbor Statement**

Any statements contained in this press release that are not statements of historical fact, including the performance, rating, availability, reliability, operation and suitability of products for various applications, may be deemed to be forward-looking statements. There are a number of important factors that could cause the results of IXYS to differ materially from those indicated by these forward-looking statements, including, among others, risks detailed from time to time in the Company's SEC reports, including its Form 10-K for the fiscal year ended March 31, 2012. The Company undertakes no obligation to publicly release the results of any revisions to these forward-looking statements.